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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. TANABE, et al.
Serial No.: 10/774,588
Filed: FEBRUARY 10, 2005
Title: METHOD FOR FABRICATING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group AU: 2891
Examiner: Victor V. Yevsikov
Confirm. No.: 1085

AMENDMENT

Mail Stop: **AMEND – FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

October 17, 2005

SIR:

In response to the Office Action mailed June 15, 2005, the period for response having been extended for one (1) month by the attached Petition for Extension of Time, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.